

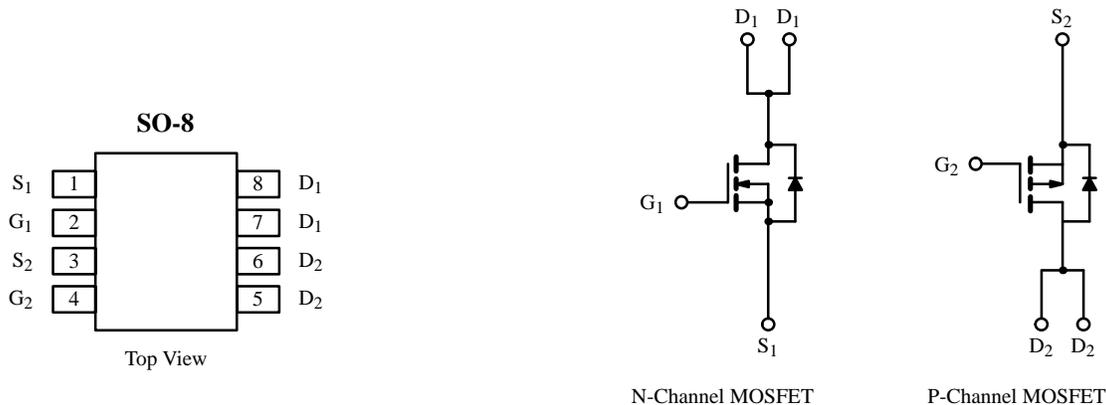
Dual Enhancement-Mode MOSFET (N- and P-Channel)

Product Summary

	V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
N-Channel	30	0.05 @ V _{GS} = 10 V	± 3.5
		0.07 @ V _{GS} = 6 V	± 3
		0.08 @ V _{GS} = 4.5 V	± 2.5
P-Channel	-30	0.10 @ V _{GS} = -10 V	± 3.5
		0.12 @ V _{GS} = -6V	± 3
		0.16 @ V _{GS} = -4.5 V	± 2.5

Recommended upgrade: Si4539DY

Lower profile/smaller size—see Si6543DQ



Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Parameter	Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage	V _{DS}	30	-30	V	
Gate-Source Voltage	V _{GS}	± 20	± 20		
Continuous Drain Current (T _J = 150°C) ^a	I _D	T _A = 25°C	± 3.5	A	
		T _A = 70°C	± 2.8		
Pulsed Drain Current	I _{DM}	± 20	± 20	A	
Continuous Source Current (Diode Conduction) ^a	I _S	1.7	-1.7		
Maximum Power Dissipation ^a	P _D	T _A = 25°C	2.0		W
		T _A = 70°C	1.3		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150		°C	

Thermal Resistance Ratings

Parameter	Symbol	N- or P- Channel	Unit
Maximum Junction-to-Ambient ^a	R _{thJA}	62.5	°C/W

Notes

a. Surface Mounted on FR4 Board, t ≤ 10 sec.

Updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #70146.

Si9939DY

Specifications ($T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

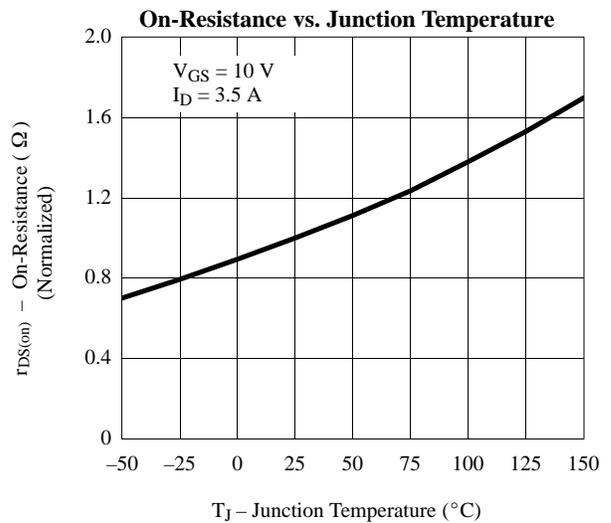
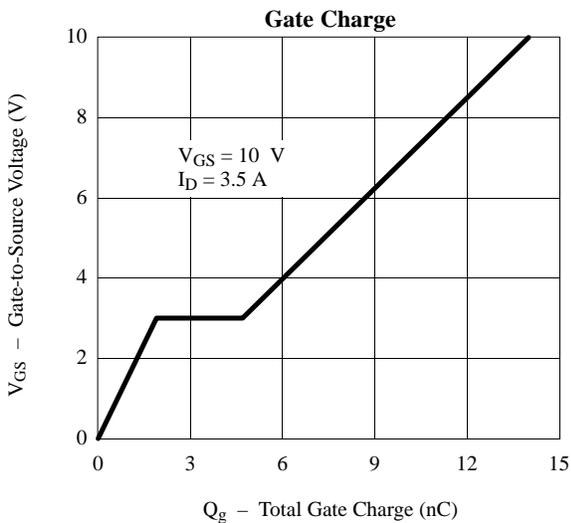
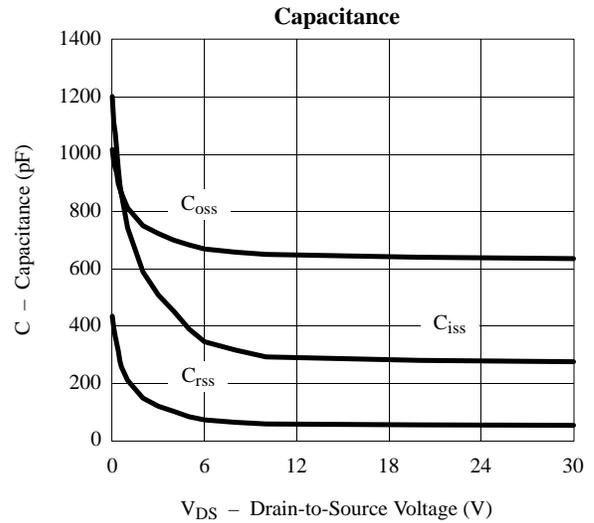
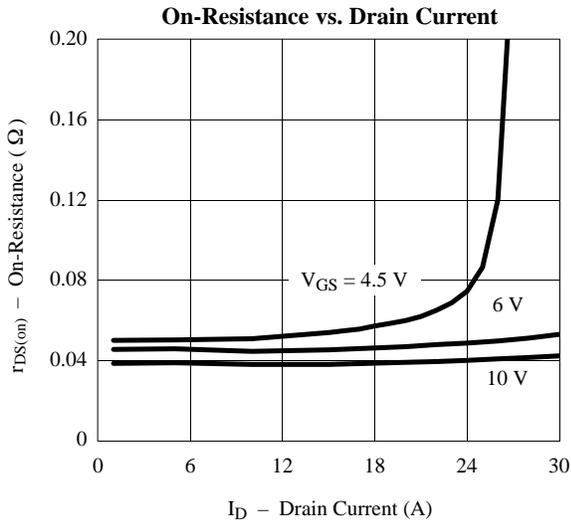
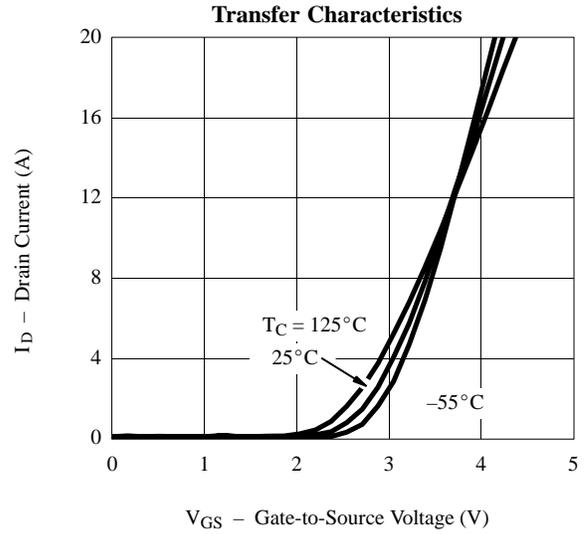
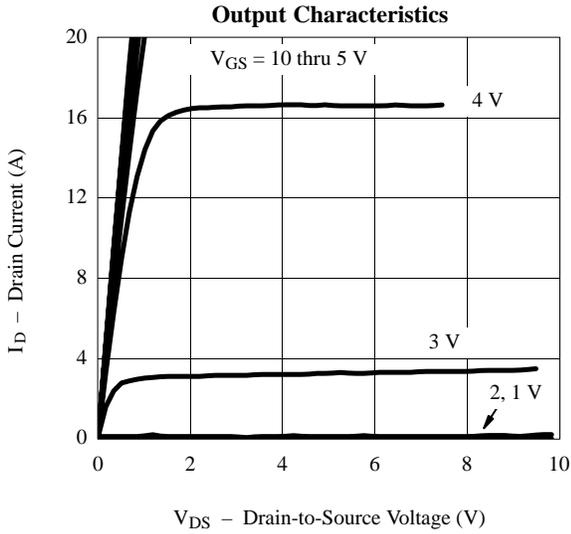
Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	1.0		V	
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-1.0			
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch		1	μA	
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch		-1		
		$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 70^\circ\text{C}$	N-Ch		5		
		$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 70^\circ\text{C}$	P-Ch		-5		
On-State Drain Current ^b	$I_{D(on)}$	$V_{DS} \geq 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch	20		A	
		$V_{DS} \leq -5 \text{ V}, V_{GS} = -10 \text{ V}$	P-Ch	-20			
		$V_{DS} \geq 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	N-Ch	3.5			
		$V_{DS} \leq -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	P-Ch	-3.5			
Drain-Source On-State Resistance ^b	$r_{DS(on)}$	$V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$	N-Ch		0.04	0.05	Ω
		$V_{GS} = -10 \text{ V}, I_D = 3.5 \text{ A}$	P-Ch		0.074	0.10	
		$V_{GS} = 6 \text{ V}, I_D = 3 \text{ A}$	N-Ch		0.045	0.07	
		$V_{GS} = -6 \text{ V}, I_D = 3 \text{ A}$	P-Ch		0.090	0.12	
		$V_{GS} = 4.5 \text{ V}, I_D = 2.5 \text{ A}$	N-Ch		0.054	0.08	
		$V_{GS} = -4.5 \text{ V}, I_D = 2 \text{ A}$	P-Ch		0.115	0.16	
Forward Transconductance ^b	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 3.5 \text{ A}$	N-Ch		9	S	
		$V_{DS} = -15 \text{ V}, I_D = -3.5 \text{ A}$	P-Ch		6		
Diode Forward Voltage ^b	V_{SD}	$I_S = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$	N-Ch		0.75	1.2	V
		$I_S = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$	P-Ch		-0.75	-1.2	
Dynamic^a							
Total Gate Charge	Q_g	N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$ P-Channel $V_{DS} = -10 \text{ V}, V_{GS} = -10 \text{ V}$ $I_D = -3.5 \text{ A}$	N-Ch		14	35	nC
Gate-Source Charge	Q_{gs}		P-Ch		14.5	35	
Gate-Drain Charge	Q_{gd}		N-Ch		1.9		
			P-Ch		2.7		
Turn-On Delay Time	$t_{d(on)}$	N-Channel $V_{DD} = 15 \text{ V}, R_L = 15 \Omega$ $I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$ P-Channel $V_{DD} = -15 \text{ V}, R_L = 15 \Omega$ $I_D \cong -1 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 6 \Omega$	N-Ch		10	30	ns
Rise Time	t_r		P-Ch		11	30	
			N-Ch		10	40	
Turn-Off Delay Time	$t_{d(off)}$		P-Ch		11	40	
			N-Ch		26	50	
Fall Time	t_f		P-Ch		30	50	
			N-Ch		10	50	
Source-Drain Reverse Recovery Time	t_{rr}		P-Ch		12	50	
		N-Ch		60	120		
			P-Ch		40	100	

Notes

- Guaranteed by design, not subject to production testing.
- Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

Typical Characteristics (25°C Unless Noted)

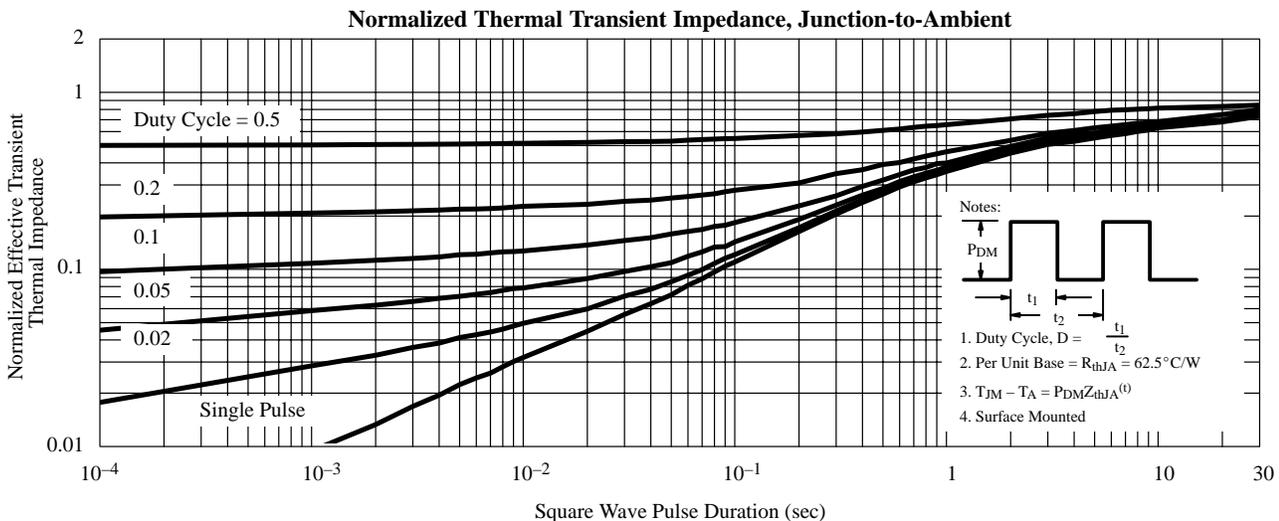
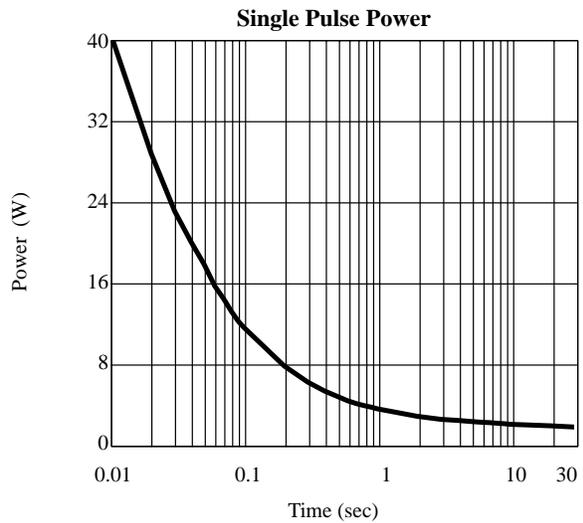
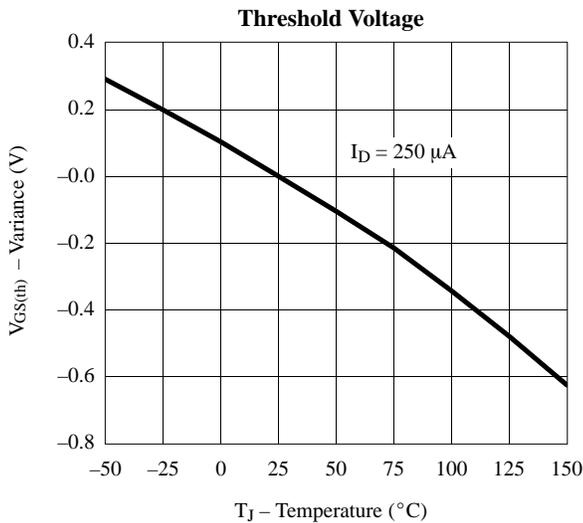
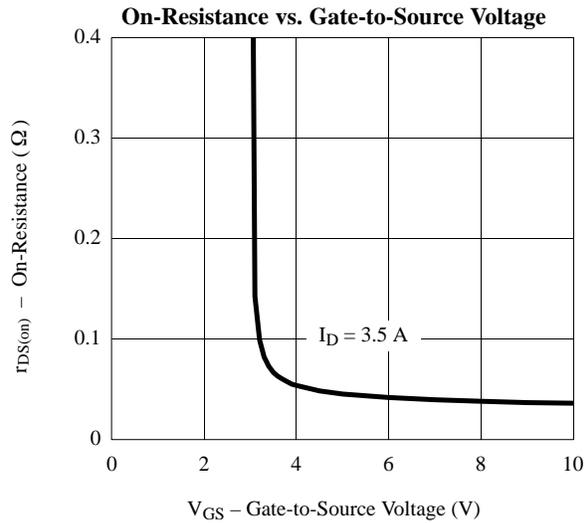
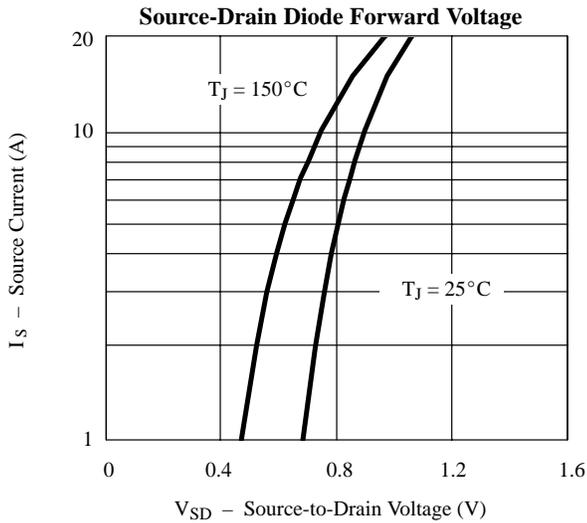
N-Channel



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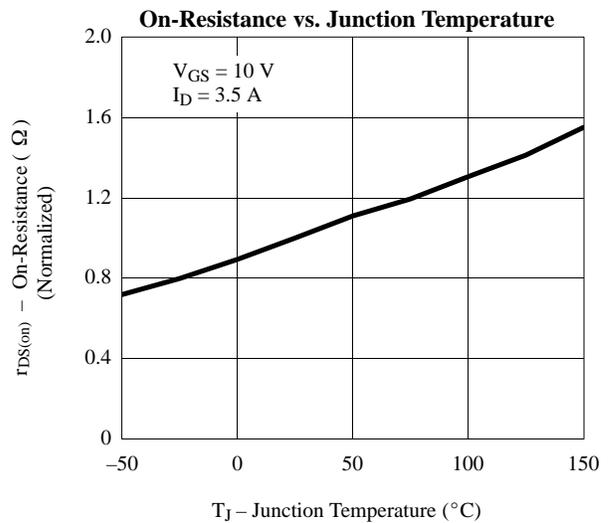
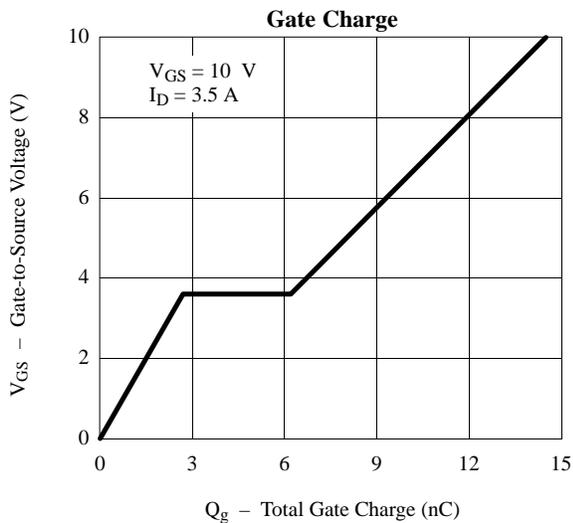
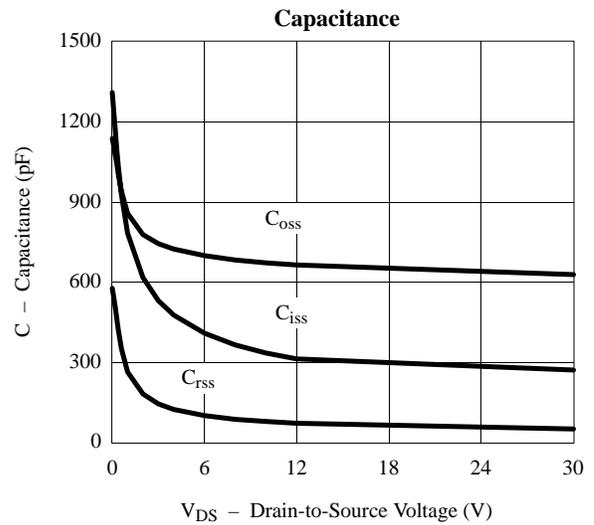
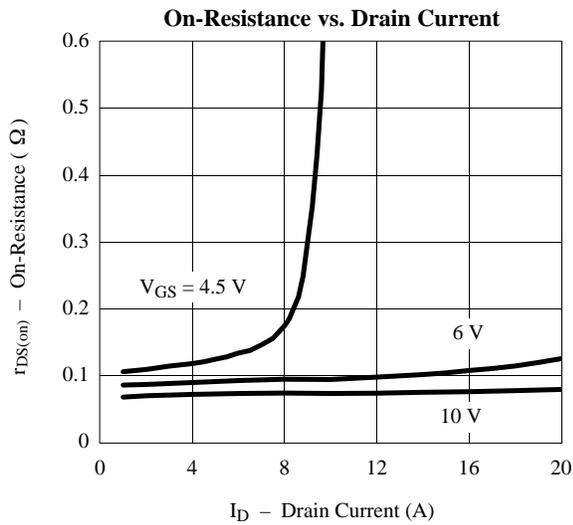
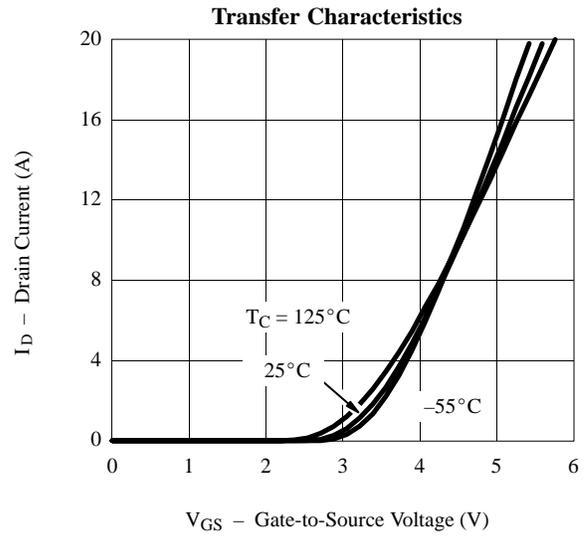
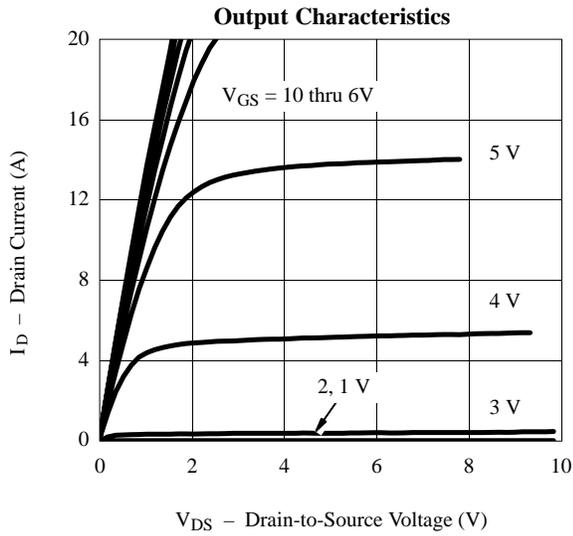
Typical Characteristics (25°C Unless Noted)

N-Channel



Typical Characteristics (25°C Unless Noted)

P-Channel



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Typical Characteristics (25°C Unless Noted)

P-Channel

