



Bucket 1 Counter set to 0x003.  
 Revolution Counter not shown.  
 Bucket Counter set to 0x0010  
 Pulse Width Counter set to 0x0003.  
 Trigger Counter set to 0x00000010.  
 Fine Counter Clock Set to Variable,  
 Pulse Width Counter Enable Selector set to Bucket Counter Terminal Count (TC),  
 Trigger Counter Clock Selector Set to Fine Counter Clock,  
 Bucket Counter Enable Selector set to Revolution CounterTerminal Count (TC),  
 Revolution Counter Enable Selector set to Event.

This Channel will output one pulse that is 3 buckets wide starting at bucket #17.

This will occur after X number of revolutions, and continue for 16 Revolutions.

This Channel can be configured to stop after 16 Revolutions (Trigger Counter Terminal Count(Halt Trigger)) or to rearm.

FineCounter Clock will be phased shifted in increments of 500ps to yield output triggers.

Timing Diagram #1 - One pulse output on each revolution for specified # of revolutions