

*If you are using a printed copy of this procedure, and not the on-screen version, then you **MUST** make sure the dates at the bottom of the printed copy and the on-screen version match. The on-screen version of the Collider-Accelerator Department Procedure is the Official Version. Hard copies of all signed, official, C-A Operating Procedures are available by contacting the **ESSHQ Procedures Coordinator, Bldg. 911A***

C-A OPERATIONS PROCEDURES MANUAL

ATTACHMENT

4.120.52.b U-Upstream & V-Target (PEER 23) Gate Tests

C-A-OPM Procedures in which this Attachment is used.		
4.120.52		

Hand Processed Changes

<u>HPC No.</u>	<u>Date</u>	<u>Page Nos.</u>	<u>Initials</u>
_____	_____	_____	_____
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Approved: _____ *Signature on File* _____
 Collider-Accelerator Department Chairman Date

V. Castillo

4.120.52.b U-Upstream & V-Target (PEER 23) Gate Tests

PASS SEMI-ANNUAL ACCEPTANCE TEST PROTOCOL

Division A Software Filename and Checksum: Title: _____ **Checksum:** _____

Division B Software Filename and Checksum: Title: _____ **Checksum:** _____

Initial testing complete:

Test Team Leader's Name (Print): _____ **Life Number:** _____

Test Team Leader's Name (Sign): _____ **Date:** ____/____/____

Acceptance test procedure complete (following repairs and retesting if required):

Test Team Leader's Name (Print): _____ **Life Number:** _____

Test Team Leader's Name (Sign): _____ **Date:** ____/____/____

Test results reviewed by:

Safety Section Head's Name (Print): _____ **Life Number:** _____

Safety Section Head's Name (Sign): _____ **Date:** ____/____/____

Test results accepted by Radiation Safety Committee:

RSC Member's Name (Print): _____ **Life Number:** _____

RSC Member's Name (Sign): _____ **Date:** ____/____/____

1.1 CONDUCT Visual check on Peer 23 gates following Table-1, below

Gate	Micro Switch		Elec Wiring	Gate Box	Lights	Gate Functions			Verify all x's Corr.	Inspn O.K. Init.
	Align	Opern				Open	Self-Closing	Latch		
UGE1									<input type="checkbox"/>	
UGII									<input type="checkbox"/>	
VTGE									<input type="checkbox"/>	

Legend: Tick = O.K. x = Problem N/A = Not Applicable

Table 1: Summary of Physical Inspection of Peer 23 Gates

1.2 Test of Entry GATE at UGE1

- VERIFY STATION PLACE** Entry Gate at UGE1 has been inspected
One Personnel **inside** gate
PEER 23 in **Controlled Access (MODE 16)**
- VERIFY** **PEER 23** is in **Controlled Access** **MODE 16**
- VERIFY OPEN** The **Exterior** gate box **Controlled Access** light is **ON**
Gate UGE1 with **Simultaneous Release** and **#6 CA Key**
- VERIFY** Simultaneous Release **Buzzer** **SOUNDS**
- VERIFY** Gate UGE1 is **OPEN**
- VERIFY** MCR sees the gate is **OPEN**
- HOLD** The Electric Strike micro switch **MADE**
- HOLD** Both of the gate micro switches **MADE**
- VERIFY RELEASE** MCR sees the gate is **NOT RESET**
Div A micro switch
- VERIFY** MCR indicates **DIV A ≠ DIV B**
- HOLD** Both of the gate micro switches **MADE**
- VERIFY RELEASE** MCR sees the gate is **NOT RESET**
Div B micro switch
- VERIFY** MCR indicates **DIV A ≠ DIV B**
- HOLD** Both of the gate micro switches **MADE**
- VERIFY RELEASE** MCR sees the gate is **NOT RESET**
The Electric Strike micro switch
- VERIFY CLOSE** MCR sees the gate is **OPEN**
The gate
- VERIFY** MCR sees the gate is **NOT RESET**
- ATTEMPT** Remote **reset** from **MCR**
- VERIFY OPEN** MCR sees Div A and Div B **RESET**
Gate from **inside**
- VERIFY CLOSE** MCR sees Div A and Div B **NOT RESET**
The gate
- PLACE** **PEER 23** in **Restricted Access (Mode 8)**
- VERIFY** **PEER 23** is in **Restricted Access** **MODE 8**
- VERIFY OPEN** The **Exterior** gate box **Restricted Access** light is **ON**
Gate UGE1 from gate box with the **Zero key**
- VERIFY OPEN** Gate UGE1 is **OPEN**
- VERIFY OPEN** Gate UGE1 with the **#6 RC CA key**
- VERIFY CLOSE** Gate UGE1 is **OPEN**
The gate

	PLACE	PEER 23 in Safe Access (Mode 2)	
<input type="checkbox"/>	VERIFY	PEER 23 is in Safe Access	MODE 2
<input type="checkbox"/>	VERIFY	The Exterior gate box Controlled Access light is	ON
	OPEN	Gate UGE1 with Simultaneous Release and Zero key	
<input type="checkbox"/>	VERIFY	Attempt to open gate UGE1 with Simultaneous Release and Zero key	FAIL
	OPEN	Gate UGE1 with Simultaneous Release and #6 CA Key	
<input type="checkbox"/>	VERIFY	Gate UGE1 is	OPEN
	CLOSE	Gate UGE1	

Check for acceptance of Test of Entry Gate at UGE1

1.3 Test of GATE at UGI1

<input type="checkbox"/>	VERIFY	Gate at UGI1 has been inspected	
	PLACE	PEER 23 in Controlled Access (MODE 16)	
<input type="checkbox"/>	VERIFY	PEER 23 is in Controlled Access	MODE 16
<input type="checkbox"/>	VERIFY	The gate box Controlled Access light is	ON
	OPEN	Gate UGI1 with Simultaneous Release and #6 CA Key	
<input type="checkbox"/>	VERIFY	Simultaneous Release Buzzer	SOUNDS
<input type="checkbox"/>	VERIFY	Gate UGI1 is	OPEN
<input type="checkbox"/>	VERIFY	MCR sees the gate is	OPEN
	HOLD	The Electric Strike micro switch	MADE
	HOLD	Both of the gate micro switches	MADE
<input type="checkbox"/>	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	Div A micro switch	
<input type="checkbox"/>	VERIFY	MCR indicates	DIV A ≠ DIV B
	HOLD	Both of the gate micro switches	MADE
<input type="checkbox"/>	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	Div B micro switch	
<input type="checkbox"/>	VERIFY	MCR indicates	DIV A ≠ DIV B
	HOLD	Both of the gate micro switches	MADE
<input type="checkbox"/>	VERIFY	MCR sees the gate is	NOT RESET
	RELEASE	The Electric Strike micro switch	
<input type="checkbox"/>	VERIFY	MCR sees the gate is	OPEN
	CLOSE	The gate with Personnel on U-Up side	
<input type="checkbox"/>	VERIFY	MCR sees the gate is	NOT RESET
<input type="checkbox"/>	VERIFY	The gate box Gate Reset light is	OFF
	RESET	The gate with #7 Sweep key at U-Up gate box	
<input type="checkbox"/>	VERIFY	MCR sees the gate is	RESET
<input type="checkbox"/>	VERIFY	The U-Up gate box Gate Reset light is	ON
	OPEN	The gate without Simultaneous Release	
<input type="checkbox"/>	VERIFY	MCR sees the gate is	OPEN
<input type="checkbox"/>	VERIFY	The U-Up gate box Gate Reset light is	OFF
	CLOSE	The gate	

- | | | | |
|--------------------------|---|---|---------------|
| <input type="checkbox"/> | PLACE | PEER 23 in Safe Access (Mode 2) | |
| <input type="checkbox"/> | VERIFY | PEER 23 is in Safe Access | MODE 2 |
| <input type="checkbox"/> | VERIFY | The gate box Controlled Access light is | ON |
| | OPEN | Gate UGI1 with Simultaneous Release and Zero key | |
| <input type="checkbox"/> | VERIFY | Attempt to open gate UGI1 with Simultaneous Release and Zero key | FAIL |
| | OPEN | Gate UGI1 with Simultaneous Release and #6 CA Key | |
| <input type="checkbox"/> | VERIFY | Gate UGI1 is | OPEN |
| | CLOSE | Gate UGI1 | |
| <input type="checkbox"/> | Check for acceptance of Test of Gate at UGI1 | | |

1.4 Reset of Entry GATE at VTGT from Peer 23 Cabinets in Bldg. 921

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|--------------------------|--|--|----------------|
| <input type="checkbox"/> | PLACE | PEER 23 in Controlled Access (MODE 16) | |
| <input type="checkbox"/> | VERIFY | PEER 23 is in Controlled Access | MODE 16 |
| | OPEN | PEER 23 Div A cab in bldg 921 | |
| | PRESS | Reset button for gate VTGE1 on remote VTGE1 box | |
| <input type="checkbox"/> | VERIFY | MCR indicates VTGE1 Div A on Mtce page | RESET |
| | SECURE | Peer 23 Div A cabinet | |
| <input type="checkbox"/> | VERIFY | Peer 23 Div A cabinet is | SECURED |
| | OPEN | Peer 23 Div B cab in bldg. 921 | |
| | PRESS | Reset button for gate VTGE1 on remote VTGE1 box | |
| <input type="checkbox"/> | VERIFY | MCR indicates VTGE1 Div B on Mtce page | RESET |
| | SECURE | Peer 23 Div B cabinet | |
| <input type="checkbox"/> | VERIFY | Peer 23 Div B cabinet is | SECURED |
| <input type="checkbox"/> | VERIFY | MCR sees VTGE1 Divs: A <input type="checkbox"/> and <input type="checkbox"/> on Mtce page | RESET |
| <input type="checkbox"/> | VERIFY | MCR sees VTGE1 on Accel Access page | RESET |
| <input type="checkbox"/> | Check for acceptance of Reset of Entry Gate at VTGT from Peer 23 Cabinets in Bldg 921 | | |

END OF TEST PROCEDURE

TTL: Sign for completion of initial testing: _____

Date: ____/____/____

TTL: Sign for completion of final testing: _____

Date: ____/____/____